Facultatea de Automatica, Calculatoare, Inginerie Electrica si Electronica

ORAR - Semestrul I - Anul universitar 2015-2016

http://www.aciee.ugal.ro/

ANUL III - ING. ELECTRONICA SI TELECOM.

ZIUA	MODUL	ORA	Ing. Electronica si Telecom. 2331		
			a	*b*	*c*
LUNI	I	08:00 - 09:50			
	II	10:00 - 11:50	Circuite integrate analogice (CIA) (C) Y106 Ş.l. R. Belea		
	III	12:00 - 13:50	Maşini şi acţionări electrice (MAE) (C) Y106 Prof. M. Găiceanu		
	IV	14:00 - 15:50	Arhitectura calc. (AC) (C) Y106 Prof. V. Nicolau / CIA (L) Y506B R. Belea	TCI (L) Y605B M. Andrei	
	V	16:00 - 17:50			
	VI	18:00 - 19:50			
MARŢI	I	08:00 - 09:50	Sisteme de achiziţie a datelor (SAD) (C) Y106 Prof. V. Nicolau		
	II	10:00 - 11:50	Transmisia şi codarea informaţiei (TCI) (C) Y106 Ş.l. M. Andrei		
	III	12:00 - 13:50	AC (L) Y602 M. Andrei	CIA (L) Y506B R. Belea / MAE (L) Y306 C. Dache	MAE (L) Y306 C. Dache / CIA (L) Y506B R. Belea
	IV	14:00 - 15:50			
	V	16:00 - 17:50			
	VI	18:00 - 19:50			

MIERCURI	I	08:00 - 09:50			
	II	10:00 - 11:50	Circuite integrate analogice (CIA) (C) Y105 Ş.l. R. Belea	CIA (S) Y105 Ş.l. R. Belea	
	III	12:00 - 13:50	Transm. şi cod. Inf. (TCI) (C) Y105 Ş.l. M. Andrei	TCI (S) Y105 Ş.l. M. Andrei	
	IV	14:00 - 15:50	Arhitectura calculatoarelor (AC) (C) Y105 Prof. V. Nicolau		
	V	16:00 - 17:50			
	VI	18:00 - 19:50			
JOI	I	08:00 - 09:50			
	II	10:00 - 11:50	CA (P) Y506B R. Belea	AC (L) Y602 M. Andrei	
	III	12:00 - 13:50	MAE (L) Y306 C. Dache	CA (P) Y506B R. Belea	AC (L) Y602 M. Andrei
	IV	14:00 - 15:50			CA (P) Y506B R. Belea
	V	16:00 - 17:50			
	VI	18:00 - 19:50			
VINERI	I	08:00 - 09:50			
	II	10:00 - 11:50			TCI (L) Y605B M. Andrei
	III	12:00 - 13:50	TCI (L) Y605B M. Andrei		
	IV	14:00 - 15:50			
	V	16:00 - 17:50			
	VI	18:00 - 19:50			

Responsabil: Andrei Mihaela Y607